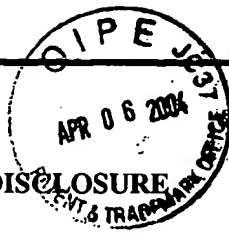


FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

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STATEMENT BY APPLICANT**

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Applicant(s)	Tarek Kaylani et al.
Group Art Unit	unknown 2611
Examiner Name	unknown Tse, Young

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/YT/		D. CHEN, "A Power and Area Efficient CMOS Clock/Data Recovery Circuit for High-Speed Serial Interfaces", IEEE Journal of Solid-State Circuits, Vol. 31, No. 8, August 1996, pages 1170-1176, New York
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EXAMINER SIGNATURE	/Young Tse/	DATE CONSIDERED	06/09/2007
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